

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:	
Barmore)	
Application No: 09/607,680)	Examiner: Casiano, A.
Filed: June 30, 2000	Art Unit: 2182
For: MOTHERBOARD EXTENSION FEATURES) TO PROVIDE PLUG AND PLAY	I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage in an envelope addressed to Commissioner of Patents, Washington, D.C. 20231 on:
INFORMATION)	Z - Z 5 - 03 Date of Deposit
Commissioner For Patents Washington, D.C. 20231	Name of Person Walling Correspondence Por S. L. C. 2.25.03 Signature Date

AMENDMENT AND RESPONSE

Dear Examiner:

In response to the Office Action mailed November 25, 2002, please enter this **RECEIVED** MAR 0 6 2003 amendment and consider the following remarks.

Technology Center 2100

IN THE CLAIMS

(Unchanged) A system comprising:

a motherboard to have a chipset coupled thereto;

a memory to store a sequence of instructions, the memory coupled with the

motherboard; and

a riser card coupled with the mother board, the riser card having a circuit thereon that interacts with a corresponding portion of the chipset to provide a functionality to the